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Notice of Allowability	Application No.	Applicant(s)	
	10/619,278	WU ET AL.	
	Examiner	Art Unit	
	Khanh Tran	2611	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication IGHTS. This application is subject to	plication. If not included will be mailed in due course. THIS	rе
1. \boxtimes This communication is responsive to <u>the Amendment After</u>	Final filed on 06/18/2007 .		
2. ☑ The allowed claim(s) is/are <u>1-36, 38-42, 44-63, 65-71, 73-7</u> Office action.	76, 78-79 and 81-86, which have bee	en renumbered as set forth in the	
 Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 	e been received. e been received in Application No cuments have been received in this of this communication to file a reply	national stage application from the	
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			
 CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date leach sheet. Replacement sheet(s) should be labeled as such in the such sheet. 	son's Patent Drawing Review (PTO s Amendment / Comment or in the C .84(c)) should be written on the drawin	Office action of ngs in the front (not the back) of	
DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATERIAL r	nust be submitted. Note the	
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☒ Examiner's Amendr 8. ☒ Examiner's Stateme 9. ☐ Other	(PTO-413), te	

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1. The Amendment After Final filed on 06/18/2007 has been entered. Claims 1-36, 38-42, 44-63, 65-71, 73-76, 78-79 and 81-86 are pending in this Office action.

2. Claims have been renumbered as shown below:

claims 7-8 renumbered as claims 2-3; claim 79 renumbered as claim 4; claims 81-86 renumbered as claims 5-10; claims 2-6 renumbered as claims 12-16; claims 9-10 renumbered as claims 17-18; claims 12-14 renumbered as claims 19-21; claims 15-36 renumbered as claims 22-43; claims 38-42 renumbered as claims 44-48; claims 44-56 renumbered as claims 49-61; claim 58 renumbered as claim 62; claim 57 renumbered as claim 63; claims 59-63 renumbered as claims 64-68; claims 65-71 renumbered as claims 69-75; claims 73-76 renumbered as claims 76-79; and claim 78 renumbered as claim 80.

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Response to Arguments

3. Applicant's arguments, see Applicants' Remarks, filed on 06/18/2007, with respect to claims 1, 7-8, 60-63, 65-76 and 78 have been fully considered and are persuasive. The rejection of claims 1, 7-8, 60-63, 65-76 and 78 has been withdrawn.

4. Objection to claims 21, 27 and 29 has been withdrawn after Applicants correct all informalities.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

5. Regarding claim 1, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "said clock phase adjustment circuit comprising parallel first and second clock phase adjustment paths configured to receive said clock phase information, said first clock phase adjustment path comprising a first multiplier and providing a first clock phase adjustment path signal, and said second clock phase adjustment path comprising a second multiplier and a first integrator and providing a second clock phase adjustment path signal".

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6. Regarding claim 11, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "clock phase adjustment circuit, receiving clock phase information and providing a clock phase adjustment signal, comprising parallel first and second clock phase adjustment paths configured to receive said clock phase information, said first clock phase adjustment path comprising a first multiplier and providing a first clock phase adjustment path signal, and said second clock phase adjustment path comprising a second multiplier and a first integrator and providing a second clock phase adjustment path signal".

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- 7. Regarding claim 11, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "means for adjusting a clock phase from clock phase information comprising parallel first and second clock phase adjustment paths configured to receive said dock phase information, said first clock phase adjustment path comprising a first means for multiplying, and said second clock phase adjustment path comprising a second means for multiplying and a first means for integrating; means for adjusting a clock frequency from clock frequency information".
- 8. Regarding claim 60, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "<u>altering a frequency of said clock signal in response to said clock frequency information and altering a phase of said clock signal in response to said clock phase information</u>

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received by parallel first and second clock phase adjustment paths, said first clock

phase adjustment path comprising a first multiplier and providing a first clock phase

adjustment path signal, and said second clock phase adjustment path comprising a

second multiplier and a first integrator and providing a second clock phase adjustment

path signal".

9. Regarding claim 73, claim is allowable over prior art of record because the cited references taken individually or in combination cannot teach or suggest "altering a frequency of said clock signal in response to said clock frequency information and altering a phase of said clock signal in response to said clock phase information received by parallel first and second clock phase adjustment paths, said first clock phase adjustment path comprising a first multiplier and providing a first clock phase adjustment path signal, and said second clock phase adjustment path comprising a second multiplier and a first integrator and providing a second clock phase adjustment path signal".

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh Tran whose telephone number is 571-272-3007. The examiner can normally be reached on Monday - Friday from 08:00 AM - 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

KCT

06/22/2007

Khanh C. Tran Primary Patent Examiner, AU 2611